

**SHALLOW TRENCH ISOLATION METHOD FOR A SEMICONDUCTOR
WAFER**

Appl. No. : 09/187,197 Confirmation No. 2744
Applicant : Jacson Liu
Filed : November 3, 1998
TC/A.U. : 2822
Examiner : Guerrero, Maria F
Docket No. : MVIP0005USA
Customer No. : 27765

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FEB 03 2005

Commissioner for Patents
P.O. Box 1450
Alexandria VA 22313-1450

AMENDMENT

5 Sir:

In response to the Notice of Non-compliant Amendment of January 6, 2005, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

10 **Remarks** begin on page 7 of this paper.